

09/024771

ABSTRACT OF THE DISCLOSURE

A biasing/erasing oscillator includes a series circuit formed by an entire-width erasing head, a linear record erasing head and an inductor. The series circuit is connected in parallel with an oscillating capacitor to form an LC resonant circuit. The LC resonant circuit is connected to a base of a transistor through a direct current ~~preventive~~ ^{blocking} capacitor. The transistor has an emitter is connected to a series junction point, for example, between a sound erasing head and the inductor. A collector of the transistor is supplied with a bias voltage, while the transistor base is supplied with a bias current through a bias resistor. The direct current ~~preventive~~ ^{blocking} capacitor prevents the bias current from flowing into the ~~LC~~ ^{LC} resonant circuit.

A

5

A

10

004771-0000